## **SIEMENS**

## **Data sheet**

6ES7214-1BD23-0XB0



Figure similar

\*\*\*Spare part\*\*\* SIMATIC S7-200, CPU 224 Compact unit, AC power supply 14 DI DC/10 DO relay, 8/12 KB progr./8 KB data, PROFIBUS DP expandable

Supply voltage	
Rated value (AC)	
• 120 V AC	Yes
• 230 V AC	Yes
Load voltage L+	
<ul> <li>Rated value (DC)</li> </ul>	24 V
<ul> <li>permissible range, lower limit (DC)</li> </ul>	5 V
<ul> <li>permissible range, upper limit (DC)</li> </ul>	30 V
Load voltage L1	
<ul><li>Rated value (AC)</li></ul>	100 V; 100 V AC to 230 V AC
<ul> <li>permissible range, lower limit (AC)</li> </ul>	5 V
<ul> <li>permissible range, upper limit (AC)</li> </ul>	250 V
<ul> <li>permissible frequency range, lower limit</li> </ul>	47 Hz
<ul> <li>permissible frequency range, upper limit</li> </ul>	63 Hz
Input current	
Inrush current, max.	20 A; at 264 V
from supply voltage L1, max.	200 mA; 30 to 100 mA (240 V); 60 to 200 mA (120 V); output current for expansion modules (5 V DC) 600 mA
Encoder supply	
24 V encoder supply	
• 24 V	Yes; Permissible range: 20.4V to 28.8V
<ul> <li>Short-circuit protection</li> </ul>	Yes; electronic at 280 mA
<ul> <li>Output current, max.</li> </ul>	280 mA
Power loss	
Power loss, typ.	10 W
Memory	
Number of memory modules (optional)	1; pluggable memory module, content identical with integral EEPROM; can additionally store recipes, data logs and other files
Work memory	
<ul><li>integrated (for program)</li></ul>	12 kbyte; 8 KB with active run-time edit
integrated (for data)	8 kbyte
Backup	
• present	Yes; Program: Entire program maintenance-free on integral EEPROM, programmable via CPU; data: Entire DB 1 loaded from PG/PC maintenance-free on integral EEPROM, current values of DB 1 in RAM, retentive memory bits, timers, counters, etc. maintenance-free via high-performance capacitor; optional battery for long-term buffering
Battery	
Backup battery	

Backup time, max.	100 h; (min. 70 h at 40 °C); 200 days (typ.) with optional battery module
CPU processing times	
for bit operations, max.	0.22 µs
Counters, timers and their retentivity	
S7 counter	
Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
— lower limit	1
— upper limit	256
Counting range	
— lower limit	0
— upper limit	32 767
S7 times	
Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
— upper limit	64
Time range	
— lower limit	1 ms
— upper limit	54 min; 4 timers: 1 ms to 30 s; 16 timers: 10 ms to 5 min; 236 timers: 100 ms to 54 min
Data areas and their retentivity	
Flag	
• Size, max.	32 byte
Retentivity available	Yes; M 0.0 to M 31.7
of which retentive with battery	0 to 255, via high-performance capacitor or battery, adjustable
of which retentive without battery	0 to 112 in EEPROM, adjustable
Hardware configuration	7.0
Number of expansion units, max.	7; Only expansion modules of the S7-22x series can be used. Due to
	the limited output current, the use of expansion modules may be limited.
connectable programming devices/PCs	SIMATIC PG/PC, standard PC
Expansion modules	
<ul> <li>Analog inputs/outputs, max.</li> </ul>	35; max. 28 inputs and 7 outputs (EM) or max. 0 inputs and 14 outputs (EM)
Digital inputs/outputs, max.	168; max. 94 inputs and 74 outputs (CPU + EM)
AS-Interface inputs/outputs, max.	62; AS-Interface A/B slaves (CP 243-2)
Digital inputs	62,710 Internace 772 Glaves (61 2 10 2)
Number of digital inputs	14
Source/sink input	Yes; optionally, per group
Input voltage	,,,,,,,,,
Rated value (DC)	24 V
• for signal "0"	0 to 5 V
• for signal "1"	min. 15 V
Input current	
• for signal "1", typ.	2.5 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; all
— at "0" to "1", min.	0.2 ms
— at "0" to "1", max.	12.8 ms
for interrupt inputs	
— parameterizable	Yes; I 0.0 to I 0.3
for technological functions	
— parameterizable	Yes; (E 0.0 to E 1.5) 30 kHz
Cable length	
• shielded, max.	500 m; Standard input: 500 m, high-speed counters: 50 m
• unshielded, max.	300 m; not for high-speed signals
Digital outputs	
Number of digital outputs	10; Relays



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Short-circuit protection	No; to be provided externally
Switching capacity of the outputs	
<ul> <li>with resistive load, max.</li> </ul>	2 A
on lamp load, max.	200 W; 30 W with DC, 200 W with AC
Output voltage	
• for signal "1", min.	L+/L1
Output current	
<ul><li>for signal "1" rated value</li></ul>	2 A
<ul><li>for signal "0" residual current, max.</li></ul>	0 mA
Output delay with resistive load	
• "0" to "1", max.	10 ms; all outputs
• "1" to "0", max.	10 ms; all outputs
Parallel switching of two outputs	
• for uprating	No
Switching frequency	
of the pulse outputs, with resistive load, max.	1 Hz
Total current of the outputs (per group)	1116
all mounting positions	10 A
— up to 40 °C, max.	IVA
horizontal installation	10.4
— up to 55 °C, max.	10 A
Relay outputs	40
Number of relay outputs	10
Number of operating cycles, max.	10 000 000; mechanically 10 million, at rated load voltage 100 000
Cable length	
• shielded, max.	500 m
• unshielded, max.	150 m
Analog inputs	
Number of analog potentiometers	2; Analog potentiometer; resolution 8 bit
Encoder	
Connectable encoders	
• 2-wire sensor	Yes
<ul> <li>permissible quiescent current (2-wire sensor),</li> </ul>	1 mA
max.	
1. Interface	
Interface type	Integrated RS 485 interface
Protocols	
FIOLOCOIS	
• MPI	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s
	CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions;
• MPI	CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates
MPI     PPI     serial data exchange	CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI
MPI     PPI     serial data exchange  MPI	CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter
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MPI     serial data exchange  MPI     Transmission rate, min.     Transmission rate, max.  Integrated Functions  Counter     Number of counters	CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter  19.2 kbit/s  187.5 kbit/s  6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting direction, etc.
MPI     PPI     serial data exchange  MPI     Transmission rate, min.     Transmission rate, max.  Integrated Functions  Counter     Number of counters  Counting frequency, max.	CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter  19.2 kbit/s  19.2 kbit/s  187.5 kbit/s  6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting direction, etc.  30 kHz
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<ul> <li>between the channels</li> </ul>	Yes
between the channels, in groups of  Petertial expertion digital extracts	6 and 8
Potential separation digital outputs	Vest Delays
between the channels	Yes; Relays
between the channels, in groups of	3 and 4
Permissible potential difference	
between different circuits	500 V DC between 24 V DC and 5 V DC; 1500 V AC between 24 V DC and 230 V AC
Degree and class of protection	
IP degree of protection	IP20
Ambient conditions	
Ambient temperature during operation	
horizontal installation, min.	0 °C
<ul> <li>horizontal installation, max.</li> </ul>	55 °C
<ul> <li>vertical installation, min.</li> </ul>	0 °C
• vertical installation, max.	45 °C
Air pressure acc. to IEC 60068-2-13	
permissible range, lower limit	860 hPa
permissible range, upper limit	1 080 hPa
Relative humidity	
Operation, min.	5 %
Operation, max.	95 %; RH class 2 in accordance with IEC 1131-2
configuration / header	
configuration / programming / header	
Command set	Bit logic instructions, compare instructions, timer instructions, counter instructions, clock instructions, transmissions instructions, table instructions, logic instructions, shift and rotate instructions, conversion instructions, program control instructions, interrupt and communications
	instructions, logic stack instructions, integer maths, floating-point math instructions, numerical functions
Program processing	instructions, numerical functions
Program processing     Program organization	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms)
Program organization	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer
<ul><li>Program organization</li><li>Number of subroutines, max.</li></ul>	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms)
<ul><li>Program organization</li><li>Number of subroutines, max.</li><li>Programming language</li></ul>	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64
<ul> <li>Program organization</li> <li>Number of subroutines, max.</li> <li>Programming language</li> <li>LAD</li> </ul>	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes
<ul> <li>Program organization</li> <li>Number of subroutines, max.</li> <li>Programming language</li> <li>— LAD</li> <li>— FBD</li> </ul>	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes Yes
<ul> <li>Program organization</li> <li>Number of subroutines, max.</li> <li>Programming language</li> <li>— LAD</li> <li>— FBD</li> <li>— STL</li> </ul>	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes
<ul> <li>Program organization</li> <li>Number of subroutines, max.</li> <li>Programming language</li> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>Know-how protection</li> </ul>	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes Yes Yes
<ul> <li>Program organization</li> <li>Number of subroutines, max.</li> <li>Programming language         <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> </ul> </li> <li>Know-how protection</li> <li>User program protection/password protection</li> </ul>	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes Yes
Program organization  Number of subroutines, max.  Programming language  — LAD  — FBD  — STL  Know-how protection  User program protection/password protection  connection method / header	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes Yes Yes Yes Yes
<ul> <li>Program organization</li> <li>Number of subroutines, max.</li> <li>Programming language         <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> </ul> </li> <li>Know-how protection         <ul> <li>User program protection/password protection</li> </ul> </li> <li>connection method / header</li> <li>Plug-in I/O terminals</li> </ul>	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes Yes Yes
Program organization  Number of subroutines, max.  Programming language  LAD  FBD  STL  Know-how protection  User program protection/password protection  connection method / header  Plug-in I/O terminals  Dimensions	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes Yes Yes Yes Yes; 3-stage password protection  Yes
Program organization  Number of subroutines, max.  Programming language  — LAD  — FBD  — STL  Know-how protection  User program protection/password protection  connection method / header  Plug-in I/O terminals  Dimensions  Width	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes Yes Yes Yes Yes 120.5 mm
<ul> <li>Program organization</li> <li>Number of subroutines, max.</li> <li>Programming language         <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> </ul> </li> <li>Know-how protection         <ul> <li>User program protection/password protection</li> </ul> </li> <li>connection method / header</li> <li>Plug-in I/O terminals</li> <li>Dimensions</li> <li>Width</li> <li>Height</li> </ul>	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes Yes Yes Yes Yes  Yes 3-stage password protection  Yes  120.5 mm 80 mm
Program organization  Number of subroutines, max.  Programming language  — LAD  — FBD  — STL  Know-how protection  User program protection/password protection  connection method / header  Plug-in I/O terminals  Dimensions  Width  Height  Depth	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes Yes Yes Yes Yes 120.5 mm
Program organization  Number of subroutines, max.  Programming language  — LAD  — FBD  — STL  Know-how protection  User program protection/password protection  connection method / header  Plug-in I/O terminals  Dimensions  Width  Height  Depth  Weights	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes Yes Yes Yes Yes Yes 120.5 mm 80 mm 62 mm
Program organization  Number of subroutines, max.  Programming language  — LAD — FBD — STL  Know-how protection  User program protection/password protection  connection method / header  Plug-in I/O terminals  Dimensions  Width  Height  Depth	instructions, numerical functions free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms) 1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer 64  Yes Yes Yes Yes Yes  Yes: 3-stage password protection  Yes  120.5 mm 80 mm